

Figure 1

Coordination Interfaces

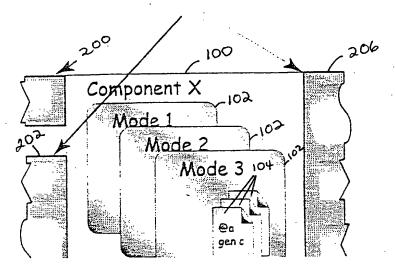


Figure 2

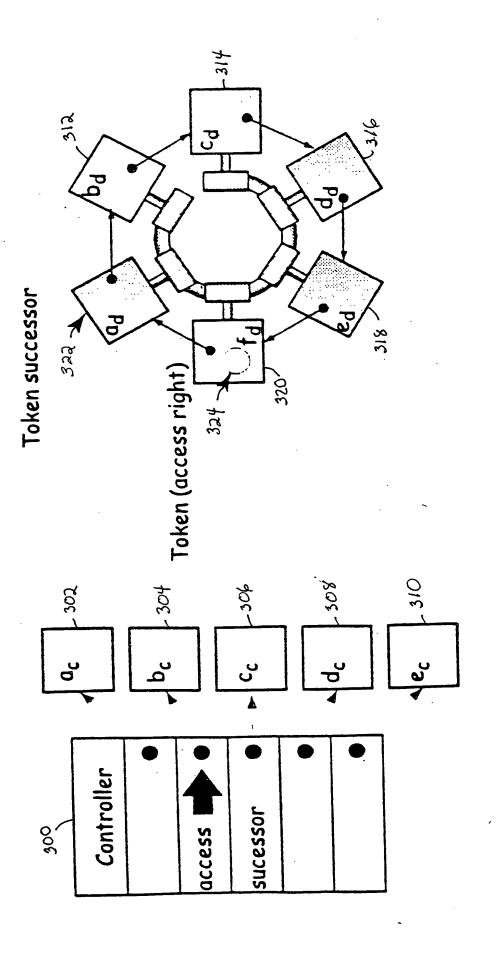


Figure 3A

Figure 3B

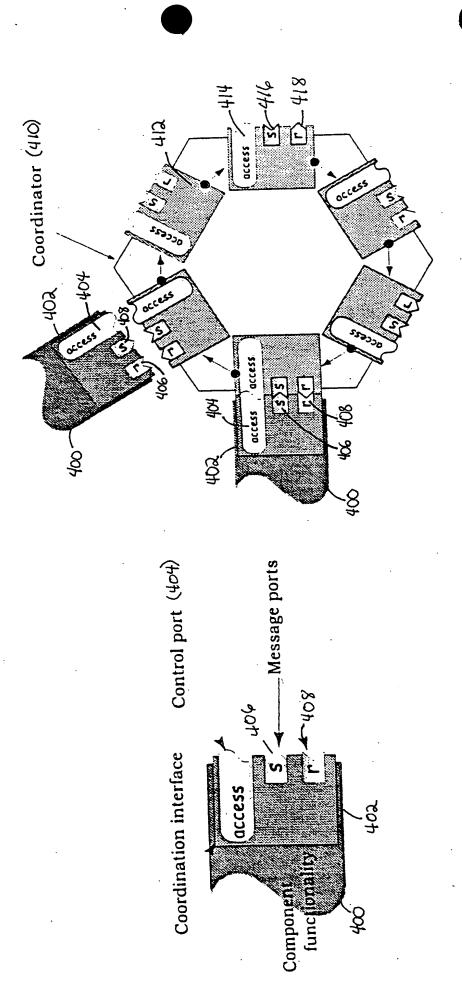


Figure 4A

Figure 4B

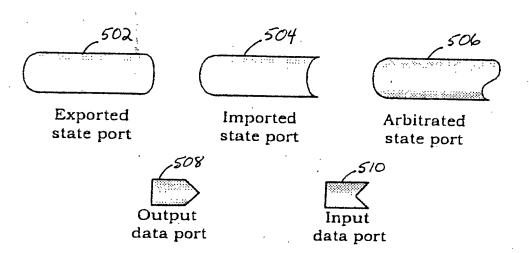


Figure 5

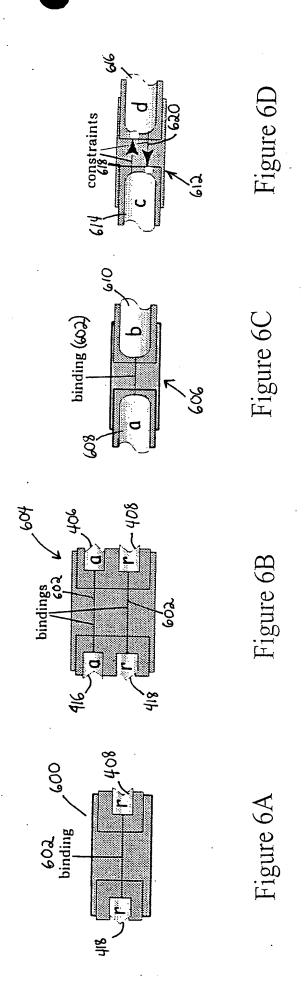


Figure 7

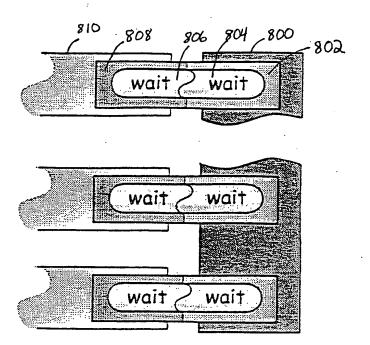


Figure 8

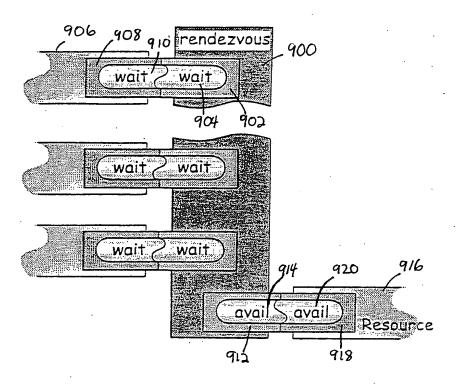


Figure 9

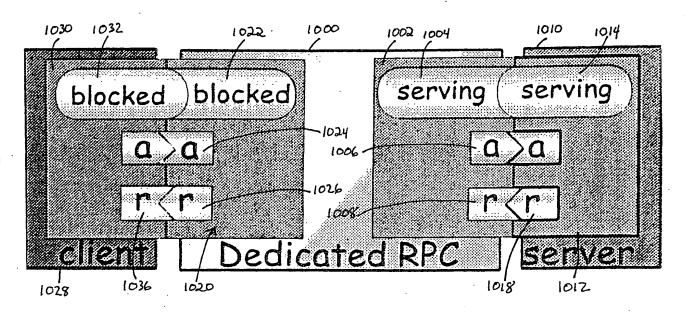
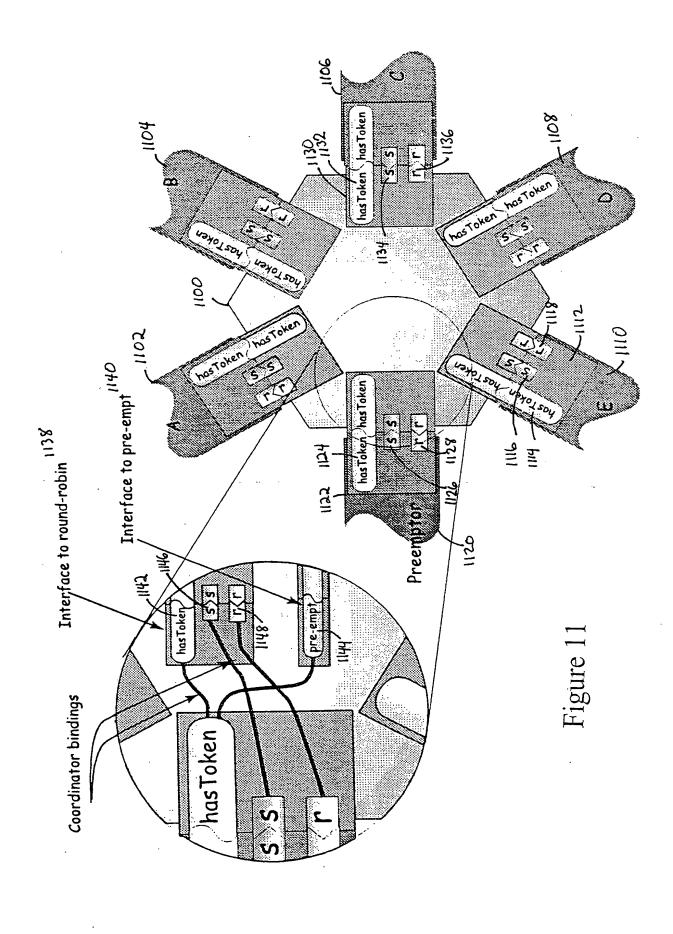


Figure 10



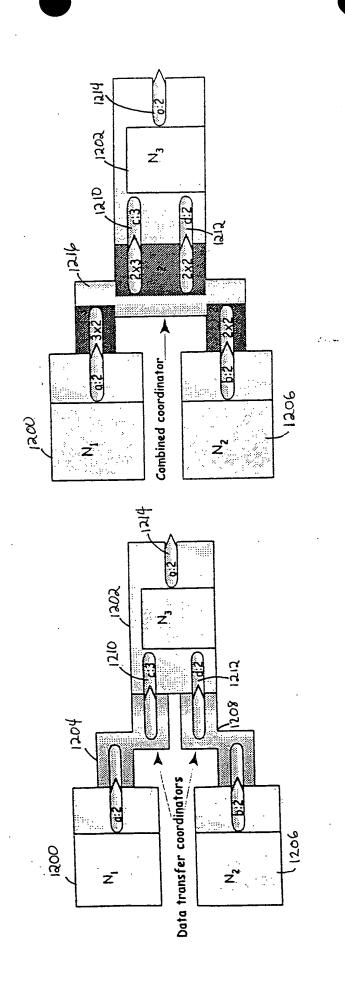


Figure 12A

Figure 12B

Figure 13

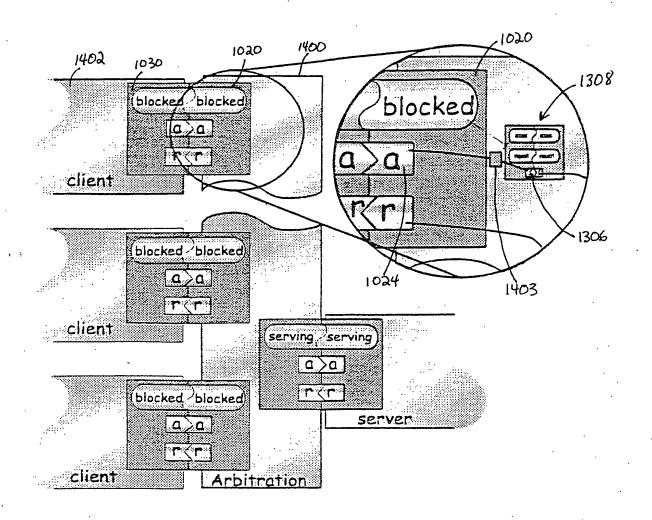


Figure 14

Figure 15

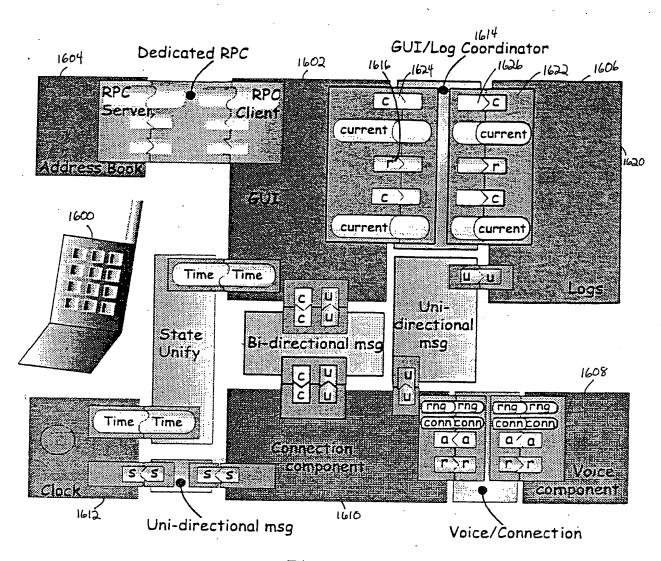


Figure 16

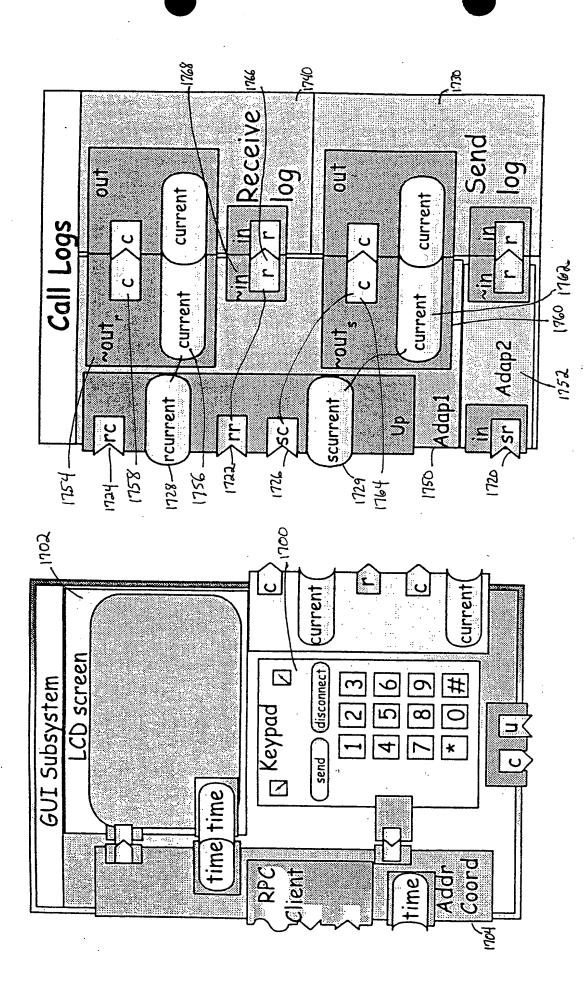


Figure 17A

Figure 17B

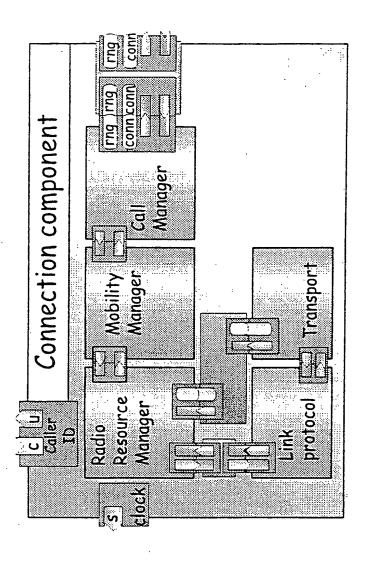


Figure 18B

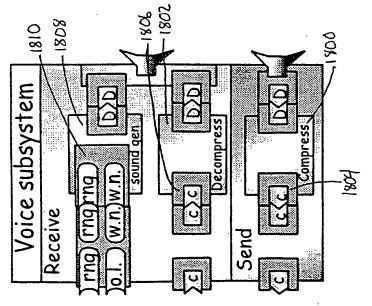


Figure 18A

Figure 19

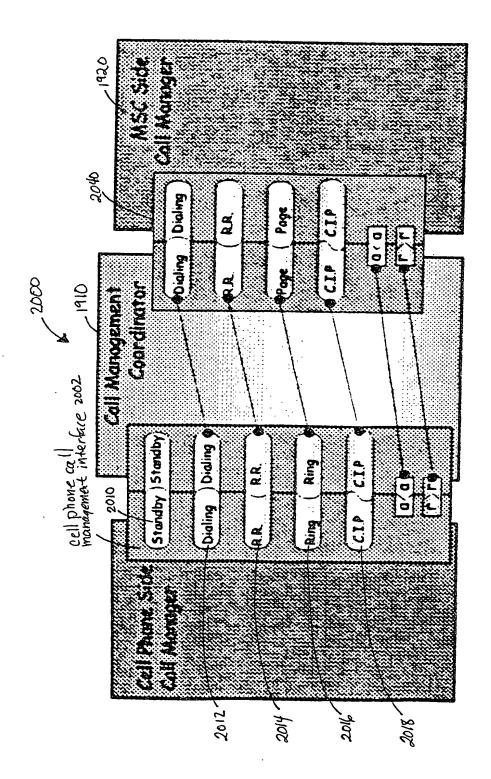


Figure 20

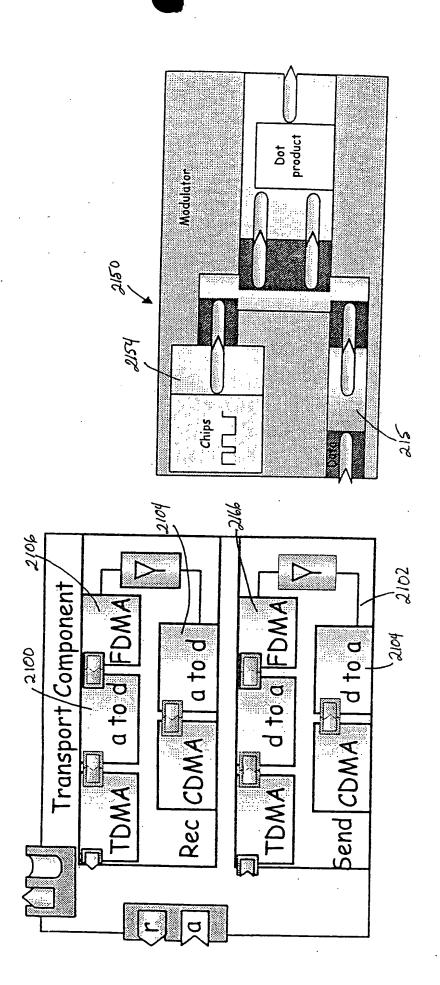


Figure 21A

Figure 21B

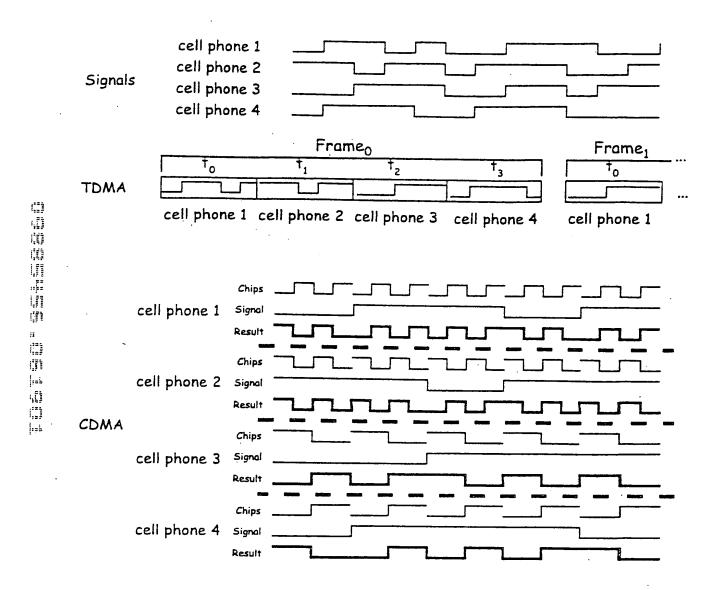


Figure 22

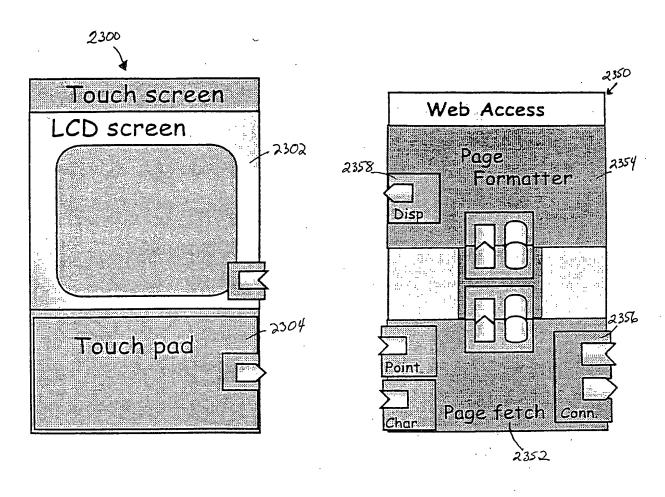


Figure 23A

Figure 23B

Figure 24A

Figure 24B



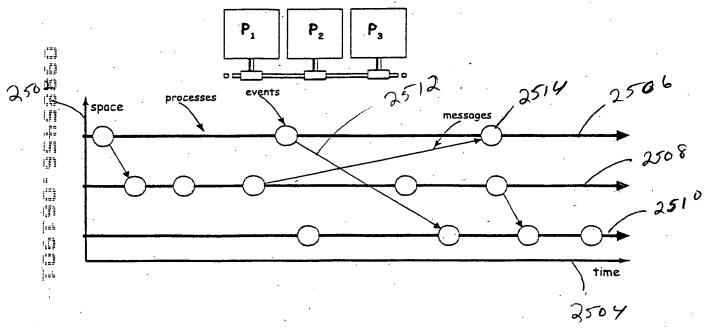


Figure 25

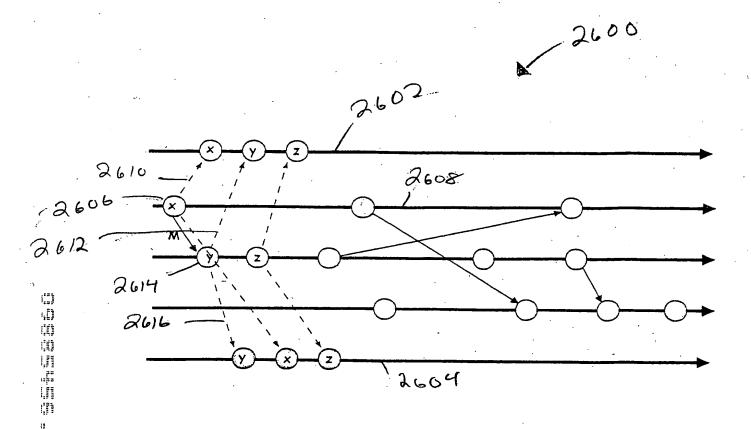


Figure 26

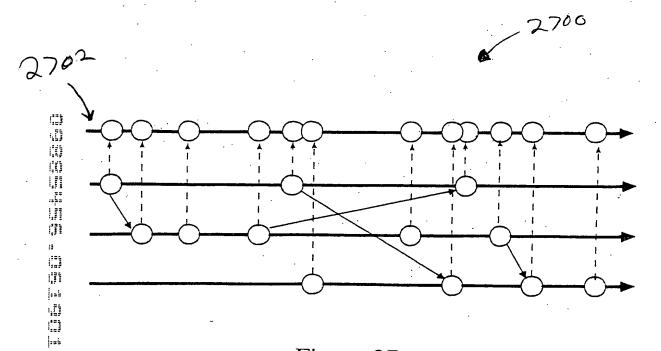


Figure 27

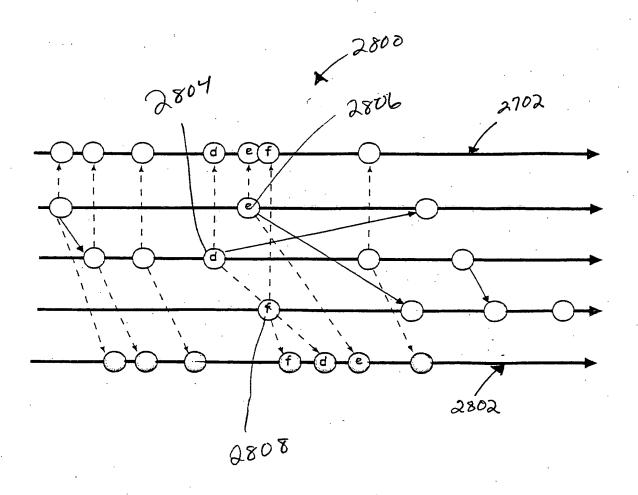


Figure 28

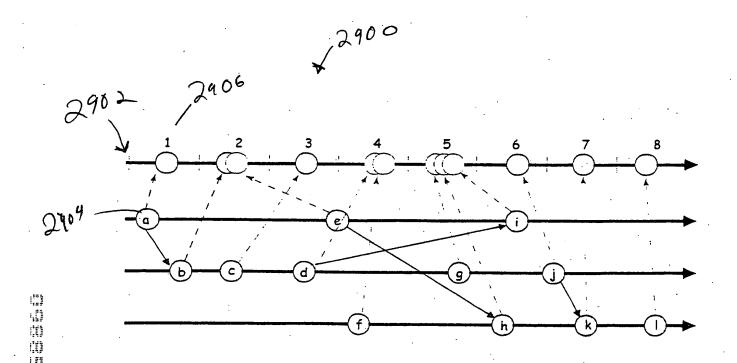


Figure 29

Figure 30

Figure 31

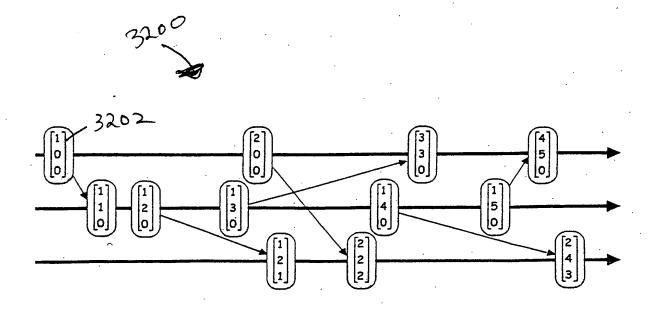


Figure 32

The first train of the first train and the first train train

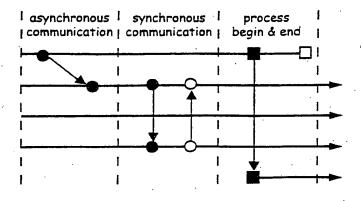


Figure 33

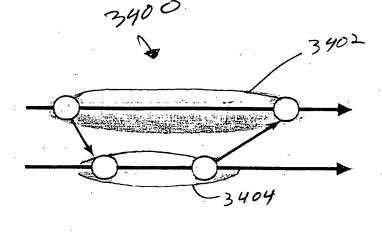


Figure 34

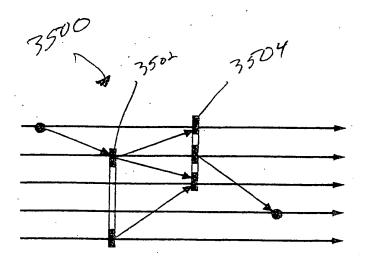


Figure 35

Figure 36



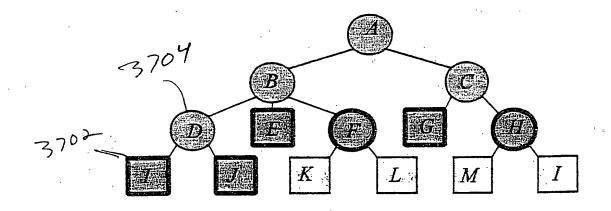
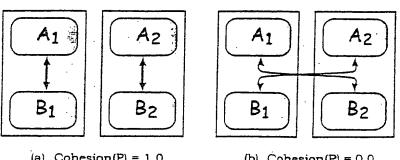
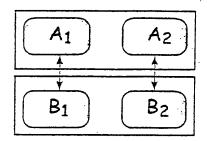


Figure 37



(a) Cohesion(P) = 1.0 Coupling(P) = 0.5

(b) Cohesion(P) = 0.0
Coupling(P) = 1.0



(c) Cohesion(P) = 1.0 Coupling(P) = 0.5

Figure 38

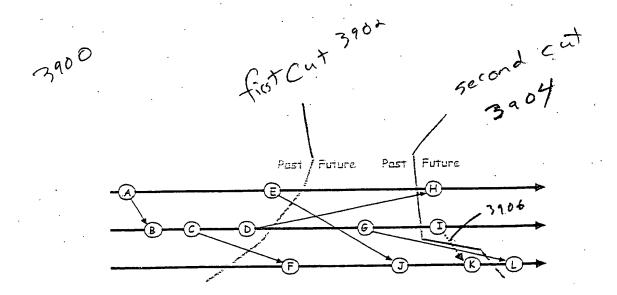


Figure 39

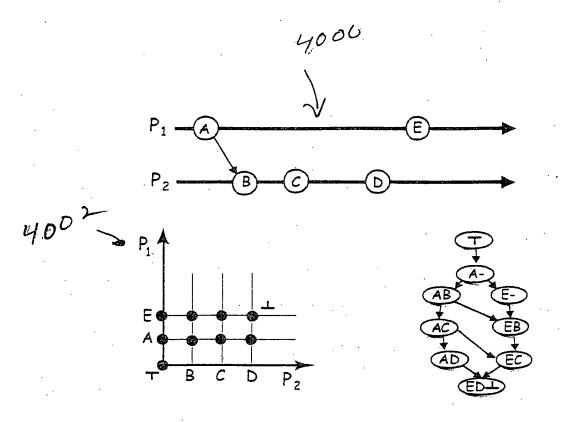


Figure 40

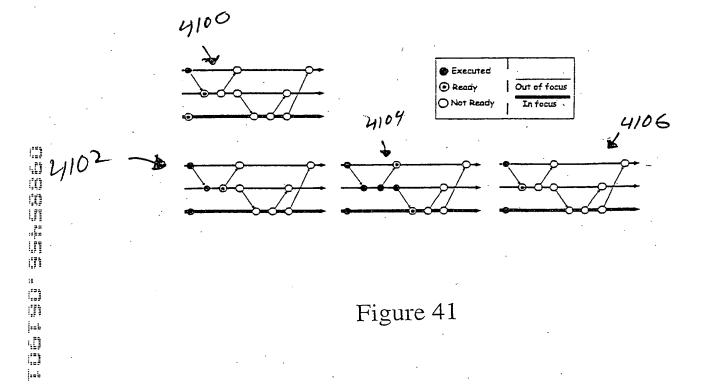


Figure 41

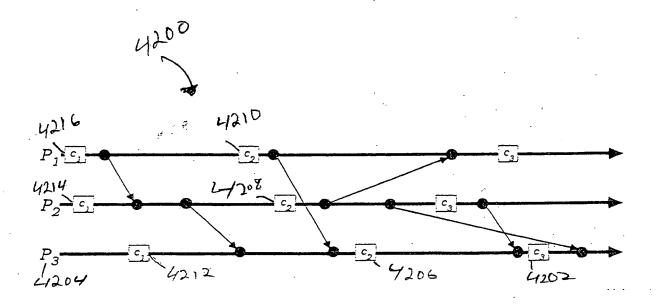


Figure 42

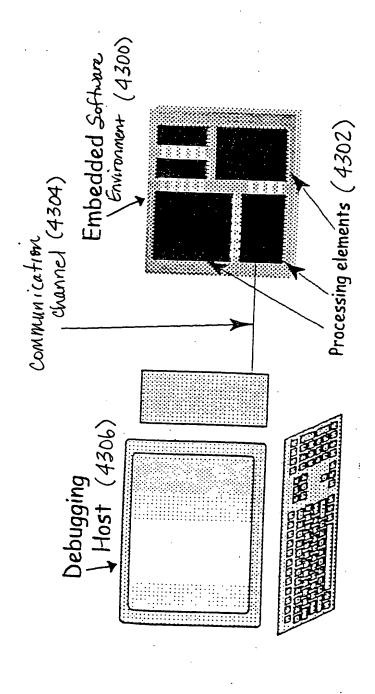


FIG. 43

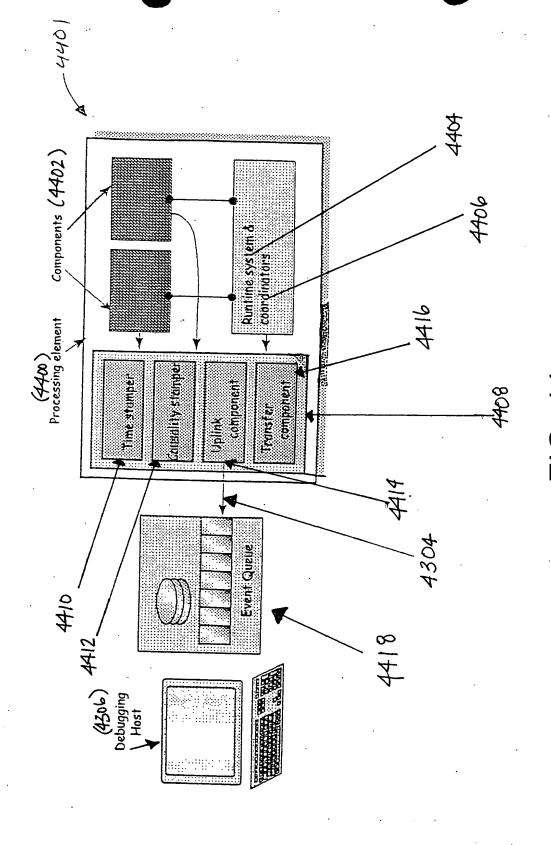


FIG. 44

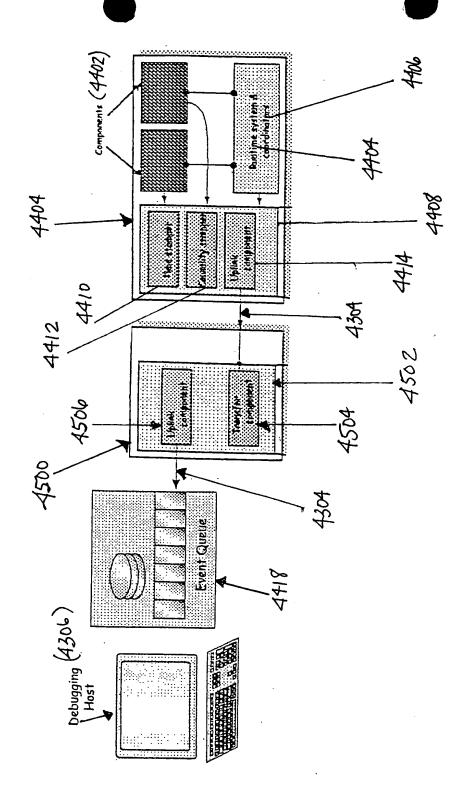


FIG. 45

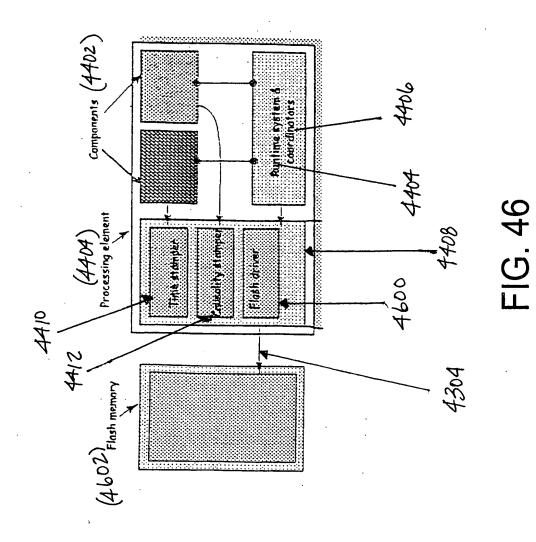
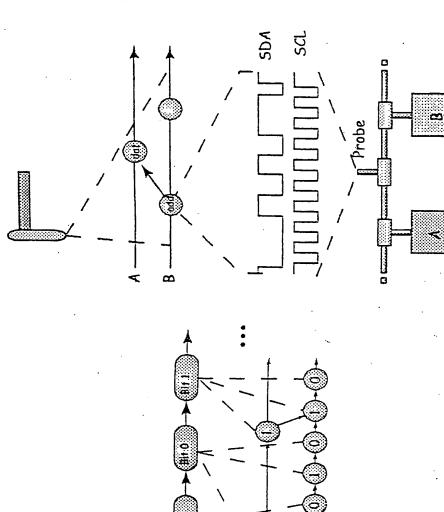


FIG. 47



SDA

SCL

Primitive recognition of first two bits

Multiple representations

FIG. 48